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5098383424 P.01/06

DEC 13 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/622,295
Confirmation No. 7253
Filing Date July 17, 2003
Inventor Joel A. Drewes
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner Erik J. Kielin
Attorney's Docket No. MI22-2342
Customer No. 021567
Title: Methods Of Forming Semiconductor Constructions

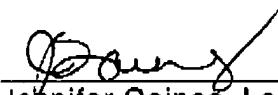
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR § 1.8

I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at (703) 872-9306 on the date shown below:

1. Certificate of Facsimile Transmission
2. Response to Office Action dated 12/2/2004

Dated: December 13, 2004

By: 

Jennifer Gaines, Legal Assistant
Telephone No. (509) 624-4276
Facsimile No. (509) 838-3424

FEE DEFICIENCY

Although it is believed that no fees are due, the Commissioner is hereby authorized to charge any fees under 37 C.F.R. 1.16 and 1.17 which may be required by this paper to Deposit Account No. 23-0925.

Dated: December 13, 2004

By: 

David G. Latwesen, Ph.D.
Reg. No. 38,533

NUMBER OF PAGES IN FACSIMILE: 6

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RESPONSE TO DECEMBER 2, 2004 OFFICE ACTION

To: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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AMENDMENTS

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